

## LTC3787 Supply Design Summary Report

***Vin : 20V (min.), 27V (nom.), 33,6V (max.)***

***Output Rails : Vout1 = 50,77V / 26A (max.)***

*Project Name : TPA3255 Boost Converter*

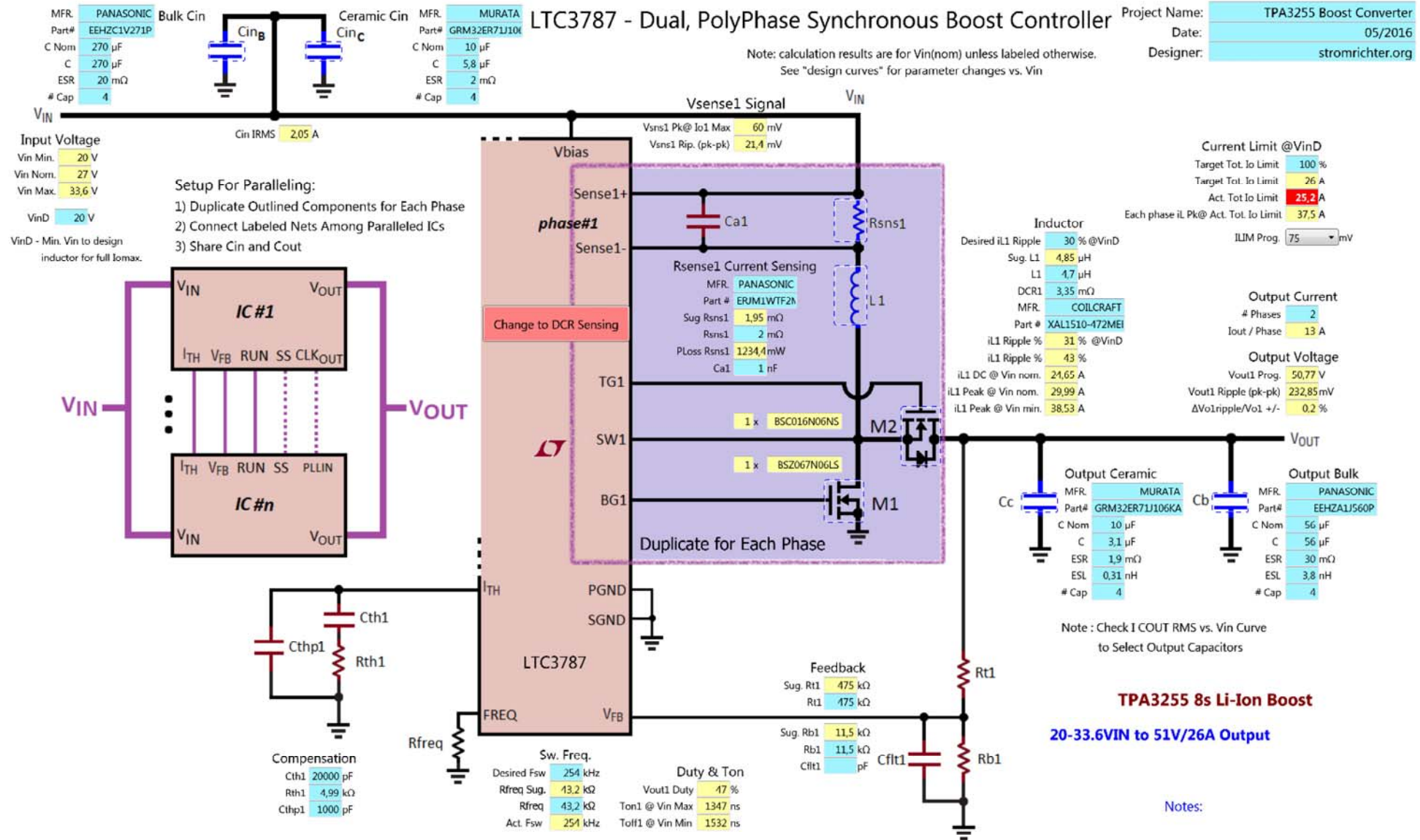
*Project Date : 05/2016*

*Designer : stromrichter.org*

# LTC3787 Solution - Simplified Schematic

Vin : 20V (min.), 27V (nom.), 33,6V (max.)

Output Rails : Vout1 = 50,77V / 26A (max.)

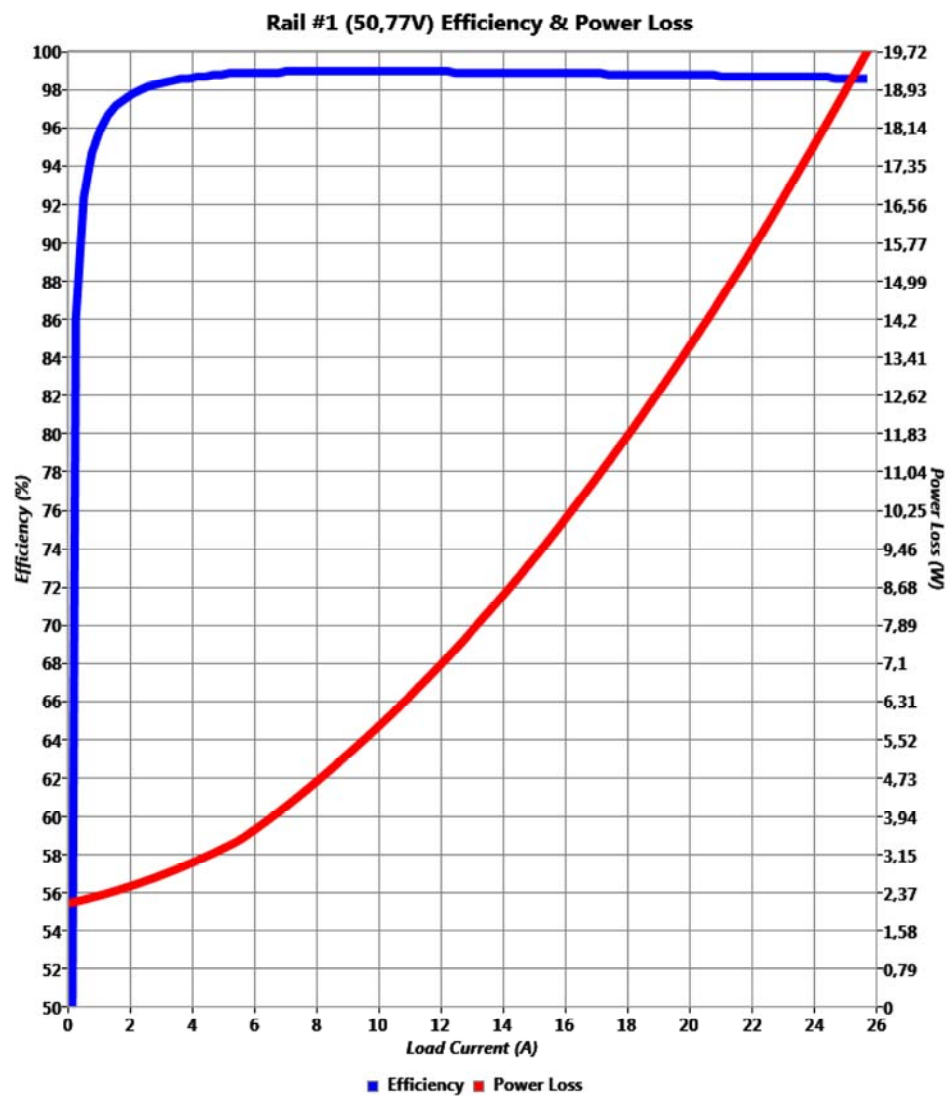


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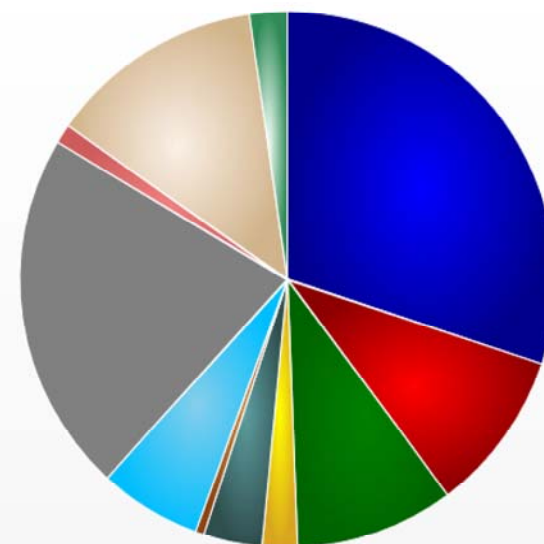
# LTC3787 Solution - Efficiency & Loss Estimations

Rail # 1 :  $V_{in} = 20V$ ,  $V_{out1} = 50,77V$

\* Estimations For CCM Mode Only. Inductor AC Losses Entered by User



**Rail #1 (50,77V) Power Loss Breakdown (Full Load)**



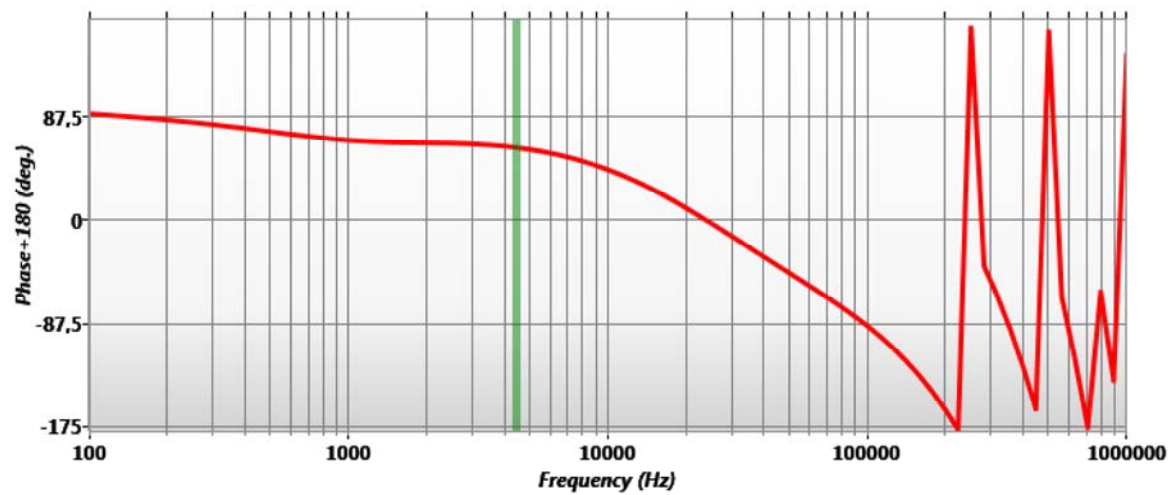
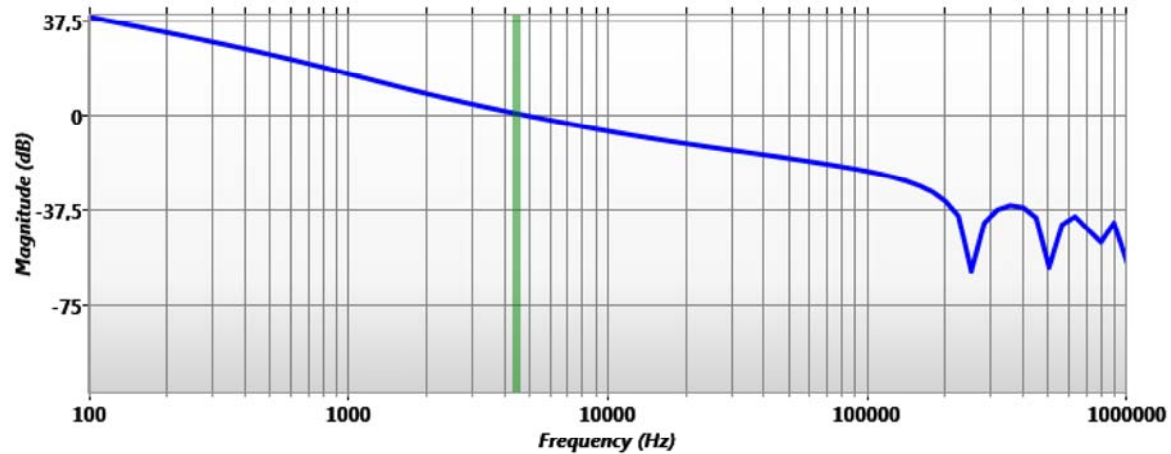
- Control Fet Conduction (10,412W,30,08%)
- Control Fet Turn On (3,365W,9,72%)
- Control Fet Turn Off (3,321W,9,6%)
- IC LDO (0,735W,2,12%)
- Sync Fet Conduction (1,226W,3,54%)
- Sync Fet Driving (0,18W,0,52%)
- Sync Fet Body Diode (2,139W,6,18%)
- Inductor DCR (7,541W,21,79%)
- Inductor Core (0,4W,1,16%)
- Rsense (4,502W,13,01%)
- Cout (0,79W,2,28%)

## LTC3787 Solution - Loop Gain & Load Transient Estimations

Rail # 1 :  $V_{in} = 20V$ ,  $V_{out1} = 50,77V$ ,  $I_{out1} = 25A$

*\* Estimations For CCM Mode Only. Estimations Based On Small Signal Avg. Model*

**Rail #1 (50,77V) Loop Gain**



# LTC3787 Solution - Summary

## LTC3787 Supply Design Summary



Project Info: TPA3255 Boost Converter, 05/2016, stromrichter.org

### Design Specifications

#### Steady State :

Rail #	Vin Min.	Vin Nom.	Vin Max.	Fsw	Vo	ΔVo rip. p-p	ΔVo rip. %	Io Max	ΔILp-p	ΔIL%	iLpk	Duty Max	Ton min.	Toff min.
1	20 V	27 V	33,6 V	254 kHz	50,77 V	232,85 mV	0,2 %	26 A	10,68 A	43 %	29,99 A	6110 %	1347 ns	1532 ns

#### Efficiency and Loop :

Rail #	Vo	Iomax	Eff.@Iomax	PLoss@Iomax	Loop BW	Loop PM
1	50,77 V	26 A	97,46 %	34,413 W	4,47 kHz	59,98 deg

#### Recommendations and Warnings :

Message	
Rail #1 Programmed Iout1 limit at VinD is lower than desired Iout1 limit (26A).	

### Power Components

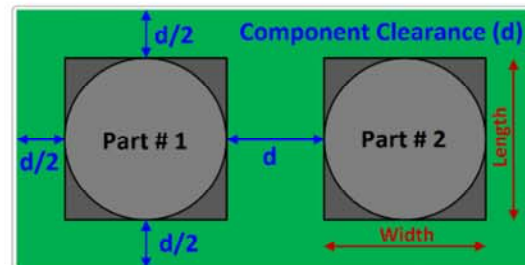
#### Power Components Bill Of Materials :

Export BOM

Ref. Des.	Value	Quantity	Description	Mfr. Name	Mfr. Part #	Pkg. (Imperial)	L(mm)	W(mm)	H(mm)	User Note
U1		1	IC	LINEAR TECH	LTC3787		5	4	0.8	
Lo1 Lo2	4,7μH	2	IND	COILCRAFT	XAL1510-472MEB		16.4	15.4	10	
Cinb1 Cinb2 Cinb3 Cinb4	270μF	4	CAP	PANASONIC	EEHZC1V271P	G	10	10	10.2	
Cinc1 Cinc2 Cinc3 Cinc4	10μF	4	CAP	MURATA	GRM32ER7J106KA12	1210	3.2	2.5	2.7	
Cob1 Cob2 Cob3 Cob4	56μF	4	CAP	PANASONIC	EEHZA1J560P	G	10	10	10.2	
Coc1 Coc2 Coc3 Coc4	10μF	4	CAP	MURATA	GRM32ER7J106KA12	1210	3.2	2.5	2.7	
Rsense1 Rsense2	2mΩ	2	RES	PANASONIC	ERJM1WTF2M0	2512	6.4	3.2	0.8	
Mctrl1 Mctrl2	60V	2	FET	Infineon	BSZ067N06LS		3.4	3.4	1.1	
Msync1 Msync2	60V	2	FET	Infineon	BSC016N06NS		6.35	5.35	1.1	

#### Power Components Footprint :

# Components	25
Max. Height	10,2 mm
Component Clearance (d)	1 mm
* Power Components Area (Excludes ICs)	1850,5 mm <sup>2</sup> 2,868 in <sup>2</sup>
* Power Components Area (Includes ICs)	1880,5 mm <sup>2</sup> 2,915 in <sup>2</sup>



#### \* Note :

The calculated power component area is only the simple sum of component footprint areas with given clearance, assuming all power components are on the same side of PCB. It is NOT the final PCB size with layout design.